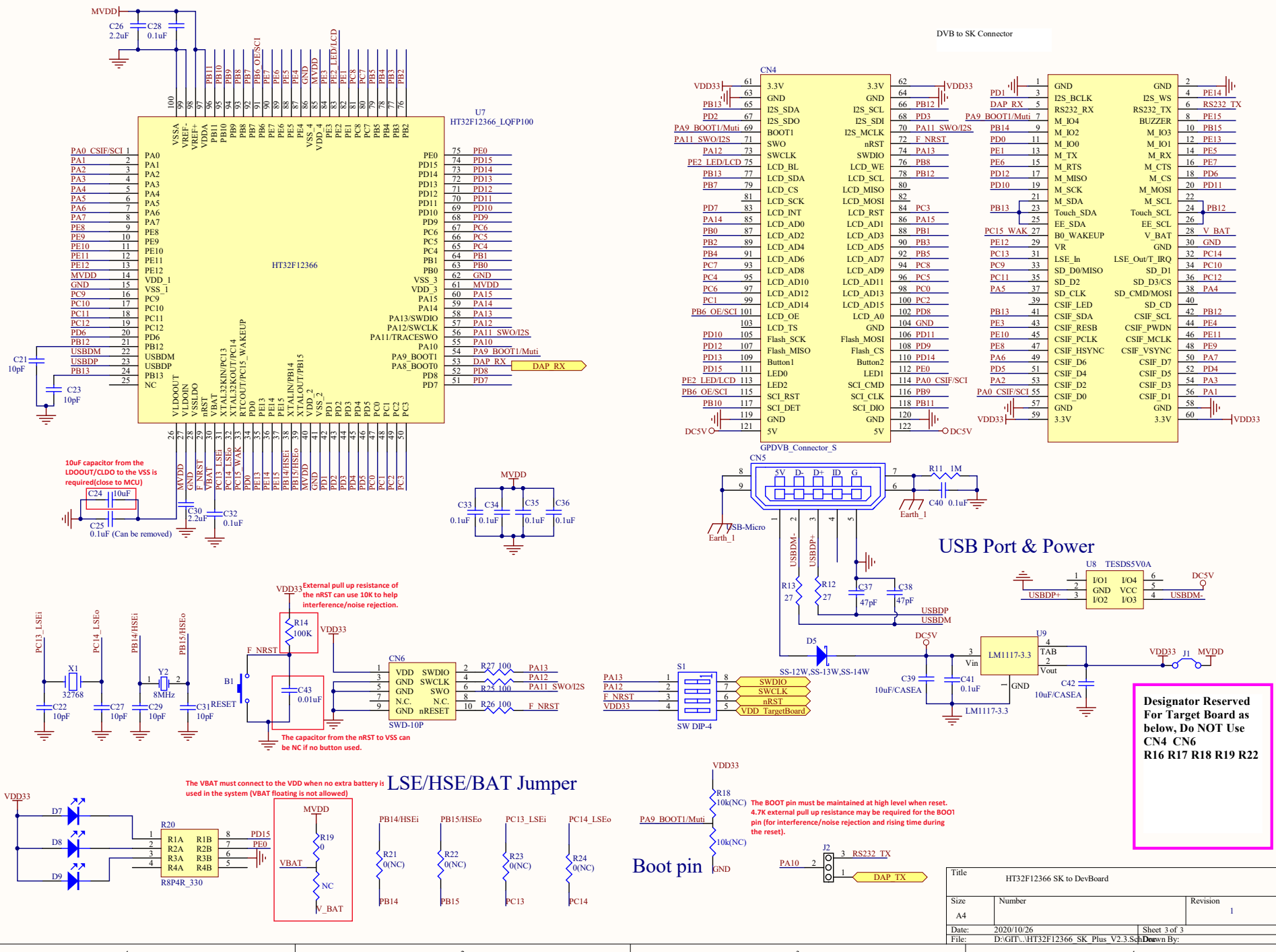
HT32F12366
U7
HT32F12366_LQFP100
DVB to SK Connector
CN4
GPDVB_Connector_S
CN5
USB Port & Power
U8 TESDS5V0A
U9
LM1117-3.3
SS-12W,SS-13W,SS-14W
SW DIP-4
SWD-10P
CN6
LSE/HSE/BAT Jumper
R8P4R_330
Boot pin
The VBAT must connect to the VDD when no extra battery is used in the system (VBAT floating is not allowed)
External pull up resistance of the nRST can use 10K to help interference/noise rejection.
The capacitor from the nRST to VSS can be NC if no button used.
10uF capacitor from the LDOOUT/CLDO to the VSS is required(close to MCU)
The BOOT pin must be maintained at high level when reset. 4.7K external pull up resistance may be required for the BOOT pin (for interference/noise rejection and rising time during the reset).
Designator Reserved For Target Board as below, Do NOT Use
CN4 CN6
R16 R17 R18 R19 R22
Title
HT32F12366 SK to DevBoard
Size
A4
Number
Revision
1
Date: 2020/10/26
Sheet 3 of 3
File: D:\GIT\..\HT32F12366_SK_Plus_V2.3.SchDoc
Drawn By: